EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2239	(program$4 or instruct$3 or request$3) with (first or primary) with thread	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:40
L2	663	((cach$3 near4 (line or data or entry or address$3)) with (modification or modified or modify or modif$4 or replac$3 or updat$3)) same (ownership or owner-ship)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:42
L3	1	(monitor near4 (address$3 or instruct$3 or request$3)) and 1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:43
L4	227	((cach$3 near4 (line or data or entry or address$3)) same (modification or modified or modify or modif$4 or replac$3 or updat$3)) and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:43
L5	39	(monitor near4 (address$3 or instruct$3 or request$3)) and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:54
L6	4	(((suspend$3 or stop$4) with (device or logic or circuit)) same thread) and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:45
L7	16	"6018759".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:48
L10	0	((suspend$3 or stop$4 or interrupt$3) with (logic or device or circuit$3)) and ((resum$3 or retain$3 or retrun$3) with (logic or device or circuit$3)) and 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:51
L12	13	((program$4 or instruct$3 or request$3) same thread) and 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:56

L13	3	((suspend$3 or stop$4 or interrupt$3) with (logic or device or circuit$3)) and 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:55
L16	8	(("6341347") or ("6351808") or ("6401155") or ("6505229") or ("6671795") or ("20030126379") or ("20030126416") or ("20030126186")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/02 15:56
L17	8	((program$4 or instruct$3 or request$3) same thread) and 16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:56
L19	90	(thread same (instruct$3 or instruction or program$4 or request$3)) and ((cach$3 with (line or entry or address$3 or data)) same ownership) and (suspend$3 or interrupt$3 or stop$4) and (resum$3 or restain$3 or return$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 16:06
L20	20	(monitor with (address$3 or request$3 or instruct$3)) and 19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 16:07
L21	15	partition$3 and 20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 16:10

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... processes and **threads** are a programming technique, intended to simplify the **logic** of ...
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Bug#264186: No sound after **suspend-resume** on IBM TP A22p
There's nothing really weird in "dmesg" after the **suspend/resume**. ... Multimedia audio
controller: Cirrus **Logic** CS 4614/22/24 [CrystalClear SoundFusion ...
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Do you control application **logic** with exception handling? ... Calling **Thread.Suspend** and
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... context records to **suspend** and **resume** tasks. A **thread** is an instance of a task. ... The
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Helper **Threads** via Virtual Multithreading
... **suspend** the main **thread**, execute a helper **thread**, and **resume** main-**thread**
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multipath-tools:Home
... the multipath(8) **cache** file **logic** in favour of a multipathd<->multipath unix ... remove over-complicated and **thread**-confusing daemon initial clone() w/ ...
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Once you understand the **logic** of the garbage collection process, it's easy to understand
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EP1224535 Sun european software patent - **Thread** switch **logic** in a ...
[0016] A processor includes anti-aliasing **logic** coupled to an L1 **cache** so that the L1
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... Next in **thread**: Felipe Alfaro Solana: "Re: APM **Suspend** Problem" ... ac97_codec: AC97
Audio codec, id: CRY20 (Cirrus **Logic** CS4297A rev B) ...
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... Next in **thread**: Ari Pollak: "Re: [PROBLEM] 2.6.3-rc1: still no **suspend/resume** on ... In
any case: given that the **logic** is right, I propose to change the ...
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[PS] [17] W. Hackbusch, editor. Multigrid Methods and Applications ...
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Sparcle's fabrication is being supported by LSI **Logic**. ... 4. switch blocking - **suspend** the
thread without unloading it, and switch to a different context. ...
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Cache block replacement scheme based on directory control bit set ...
To **resume** the once-**suspended thread**, it is necessary to identify the **thread** ... notify the
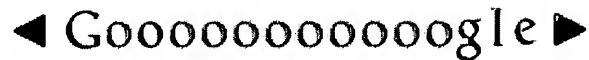processor and **logic** circuit 422f of the **cache** hit or **cache** miss. ...
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ASM **logic** as introduced in [16] and extended in [17]. ...
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Multiple **thread** multiple data predictive coded parallel processing ...
A synchronous **logic** device that reads instructions from the code memory and places the
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On receipt of an "event **suspend**" packet the special event **logic** 44 will cause ... To enable
the CPU to **resume** the interrupted **thread** after responding to the ...
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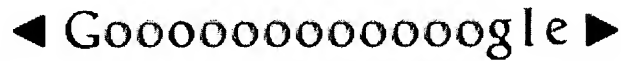[PS] A Reconfigurable Architecture for High Speed Computation by ...

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... **suspend** and **resume**) the computation of each **thread** on the pipeline in one clock
cycle ... Control **Logic** Block (PLD). (1) single **thread** Data Interconnect ...
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MBlog : Greg Wilkins
... as there is no mechanism in Java to **suspend** a **thread** and then **resume** it later. ...
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